

A VERSATILE, OBSCOLESCENCE-PROOF SYSTEM

The SIBEC 51, a versatile new single-board computer based on the INTEL 8051 family of processors, boasts more versatility and obsolescence protection than any other single-board computer available today.

In creating the SIBEC 51, the Binary Technology design team took one of the hottest new chips, the Intel 8051, and expanded its capabilities through the addition of a powerful monitor/debugger program and a fully reconfigurable board design.

The result is a single-board that was conceived and created for a variety of educational and industrial applications, including stand-alone software development, process monitoring, interaction with a host system, numerical control, incorporation into production systems, and classroom use.

FOR MICROPROCESSOR SYSTEM DEVELOPMENT...

The SIBEC 51 may be combined with any CP/M computer, and a cross-assembler, such as Avocet's XASM51, to produce a microprocessor development system that is faster, easier to use, and considerably less expensive than most of the development systems now available for the INTEL 8051 family of chips. In a SIBEC 51-based development system, programs can be written and assembled on a CP/M computer, loaded into the SIBEC 51, and tested and debugged with the SIBEC's on-board monitor/debugger.

TECHNICAL SPECIFICATIONS

The SIBEC 51 hardware (using the Intel 8031 chip) offers 11 megahertz operation, hardware multiply and divide, and two 16-bit counter/timers. It can support up to 64k of program memory in four 28-pin locations, as well as a mix of RAM and ROM in excess of 64K. Memory locations have been designed in compliance with the JEDEC standard for byte-wide memory devices to insure compatibility with all future PROM densities.

In addition, the SIBEC 51 includes a buffered expansion connector, with all processor control lines available, and 16 parallel I/O lines at a second connector. Jumper locations have been provided to configure the board for 2732, 2764, 27128 and 27256 EPROM devices as well as most "byte-wide" RAMS.

OPTIONS

For those who require non-volatile memory, the SIBEC 51 is available with an optional 2K EEPROM.

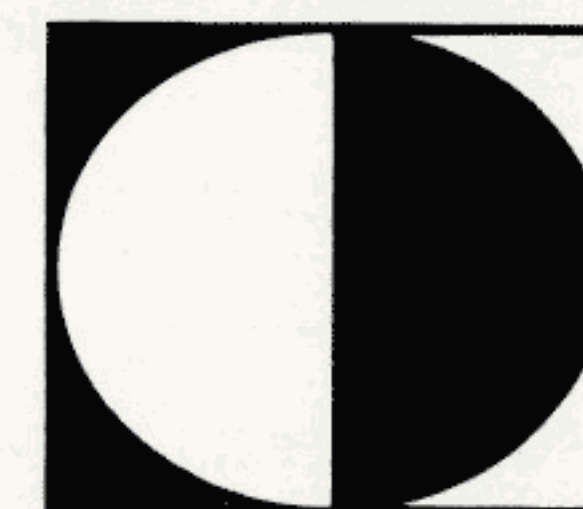
Binary Technology

P.O. BOX A-59 • HANOVER, NH 03755 • 603/643-2881



SIBEC 51

SINGLE BOARD COMPUTER



Binary Technology



THE SIBEC 51

The SIBEC 51 comes complete with a powerful 4K monitor/debugger that provides many of the utilities previously found only in large microprocessor development systems. These include:

- automatic baud rate detection and setting;
- examine and change all internal registers;
- execute from an address with break points;
- single step and trace with disassembly;
- program disassembler;
- load memory with code from the serial port; and
- many user accessible routines

The SIBEC 51 comes complete with extensive documentation, including complete hardware specifications, full monitor/debugger program description, sample program, and detailed schematic.

